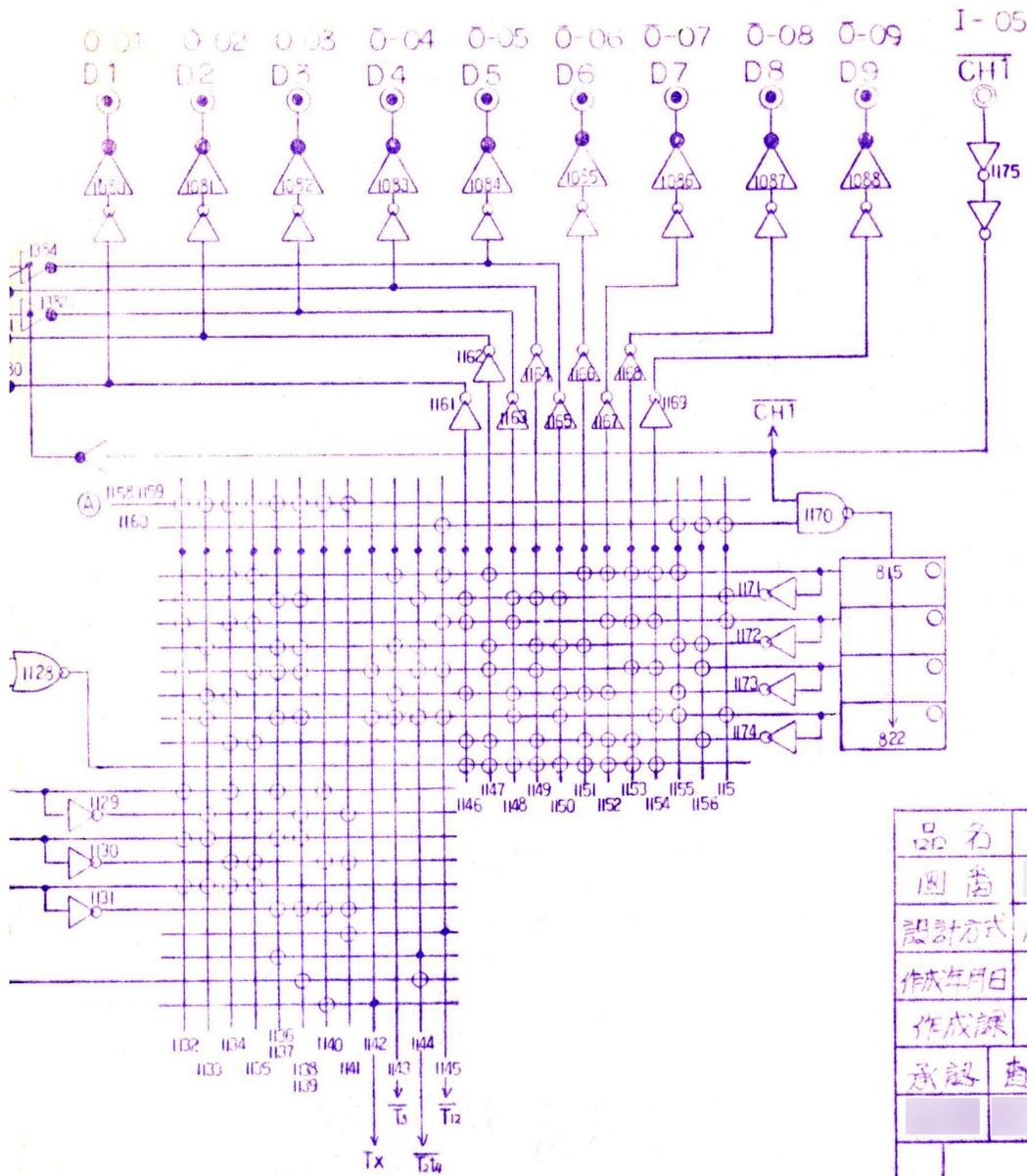
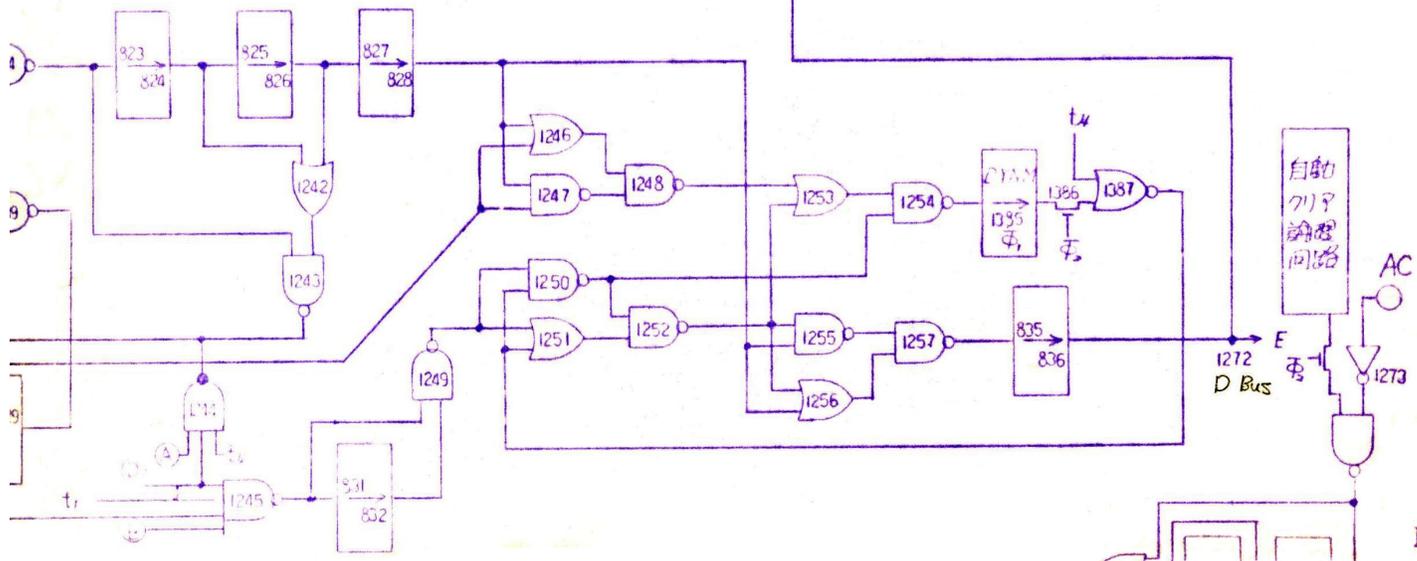
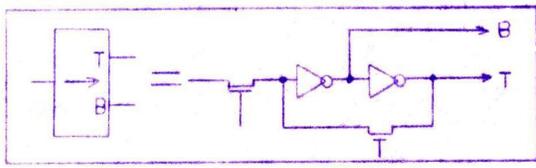


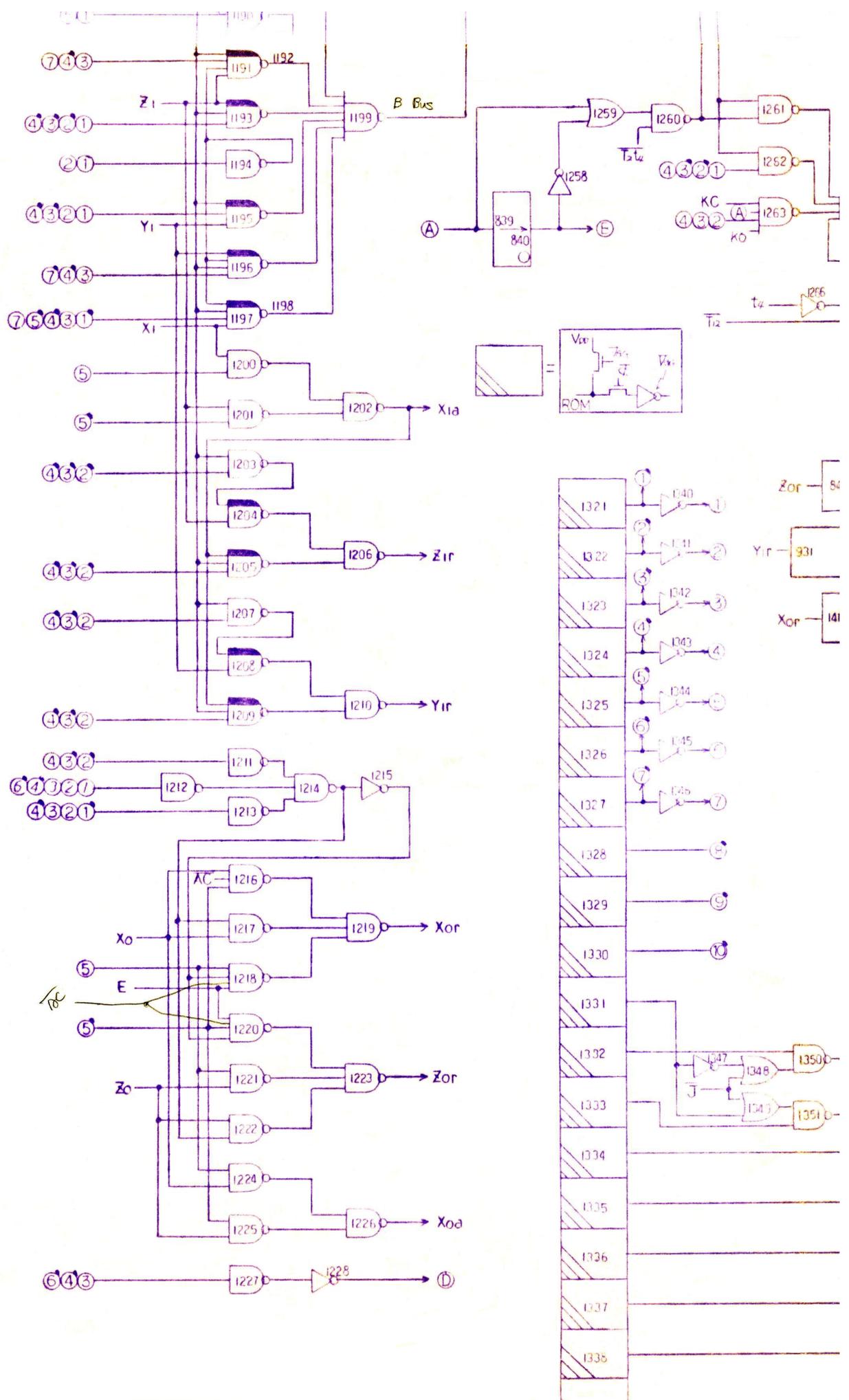
0 01
D 1
1030



1	AC	Φ
2	CH1	V _{cc}
3	CH2	V _{DD}
4	D1	DP
5	D2	SP
6	D3	SF
7	D4	SE
8	D5	SD
9	D6	SC
10	D7	SB
11	D8	SA
12	D9	KQ
13	K3	KI
14	GND	K2

品名	UPD940C		
図番			
設計方式	10.15. (7.511 B A 等)		
作成年月日	昭和49年5月10日		
作成課			
承認	査閱	4277	作成
			小口
改訂			





Page layout of μ PD940 logic schematics

Page 1	Page 2
Page 3	Page 4

On PDF Viewer,

(1) Maximize window

(2) View – Page Display – Two Page Scrolling

[Mask layout of \$\mu\$ PD940](#) based upon this logic design

[Die micrograph of \$\mu\$ PD941](#) based upon this logic design