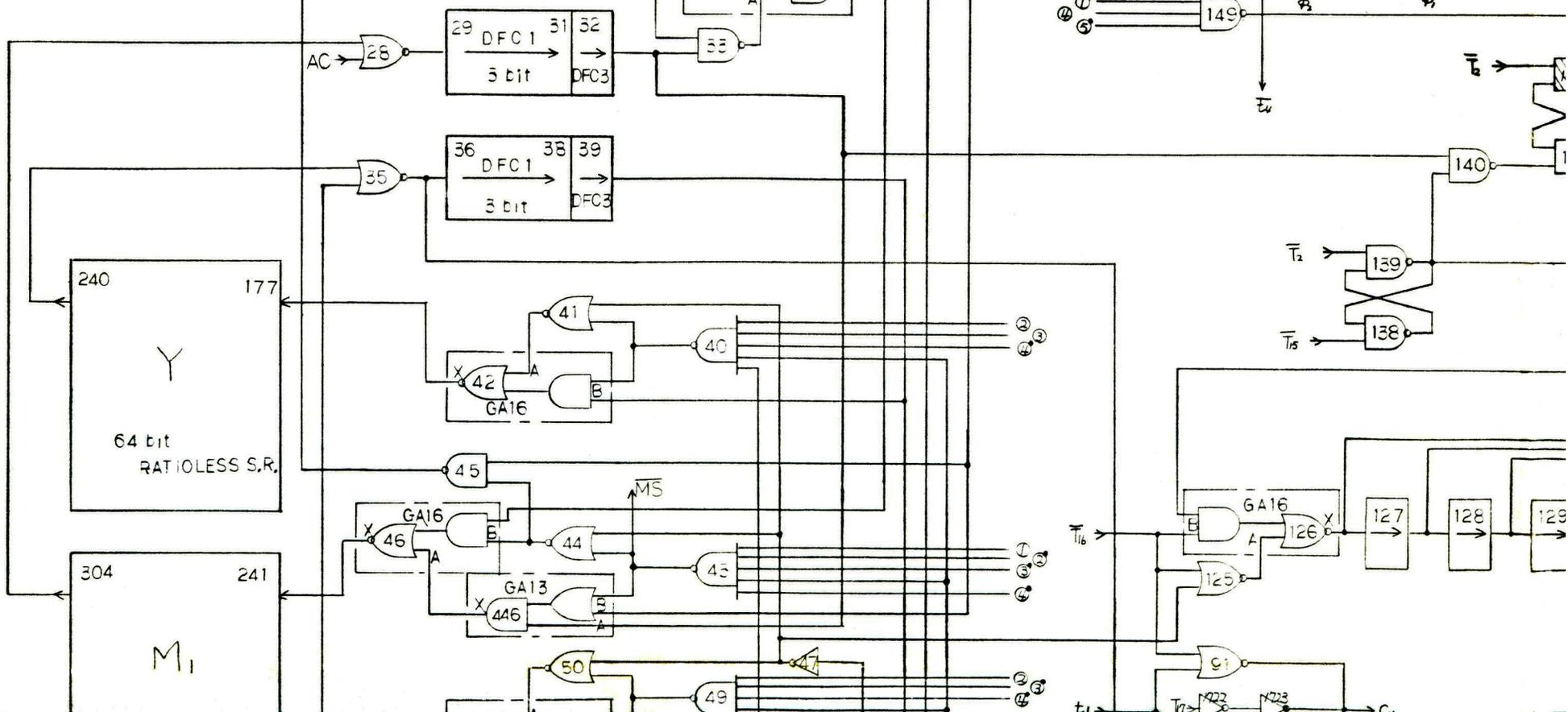
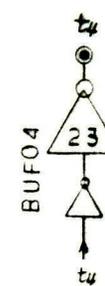
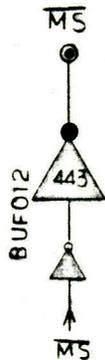
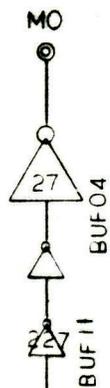
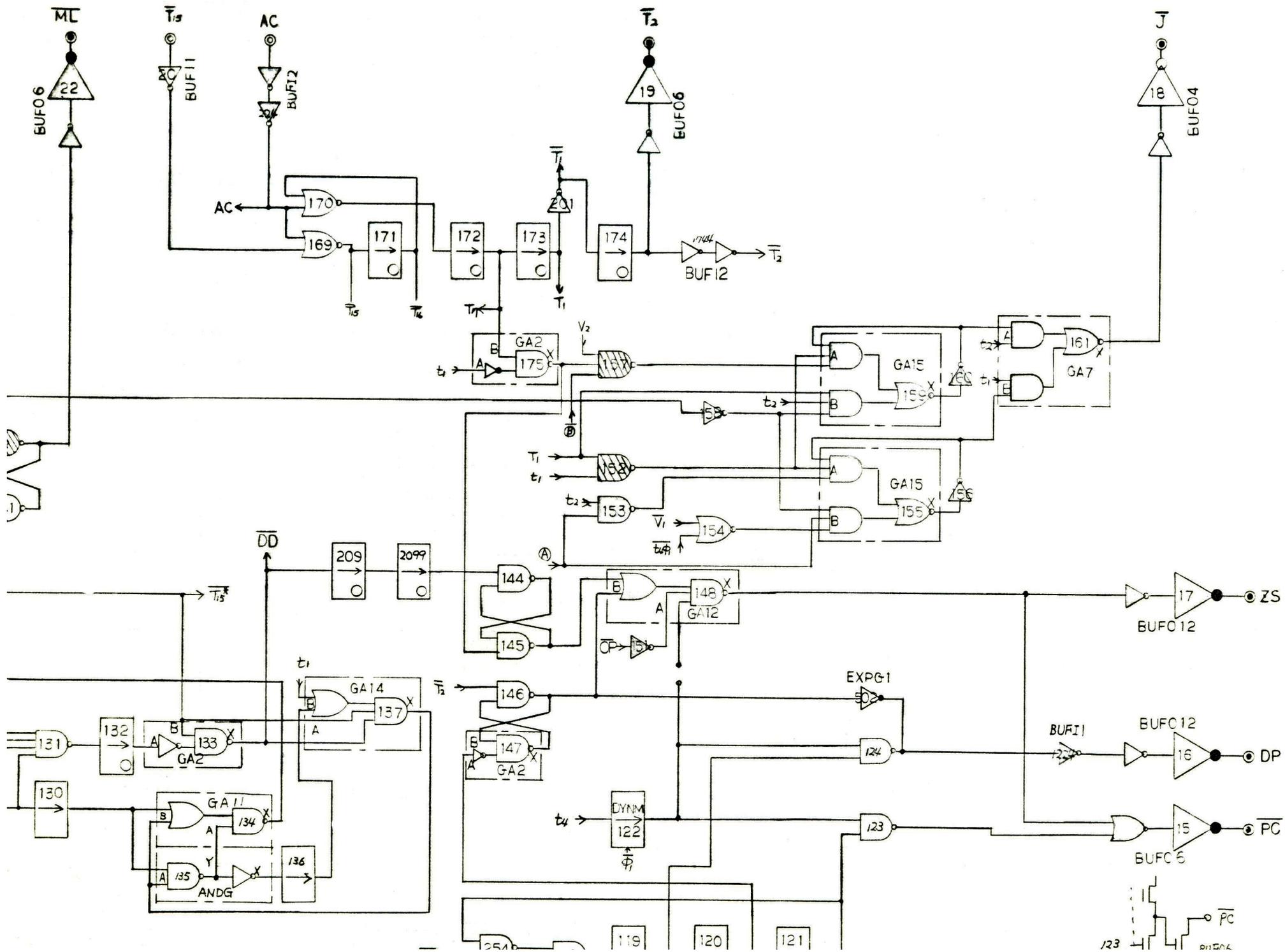


1	$\Phi_1$	$\Phi_2$	28
2	$t_p$	$V_{cc}$	27
3	J	$V_{DD}$	26
4	TC	MC	25
5	B6	PC	24
6	R1	AC	23
7	B5	ZS	22
8	B4	DP	21
9	B3	MS	20
10	B2	$\bar{T}_2$	19
11	B1	$\bar{T}_5$	18
12	X	MO	17
13	ME	CL	16
14	GND	MI	15









## Page layout of logic schematics

Page 1	Page 2
Page 3	Page 4

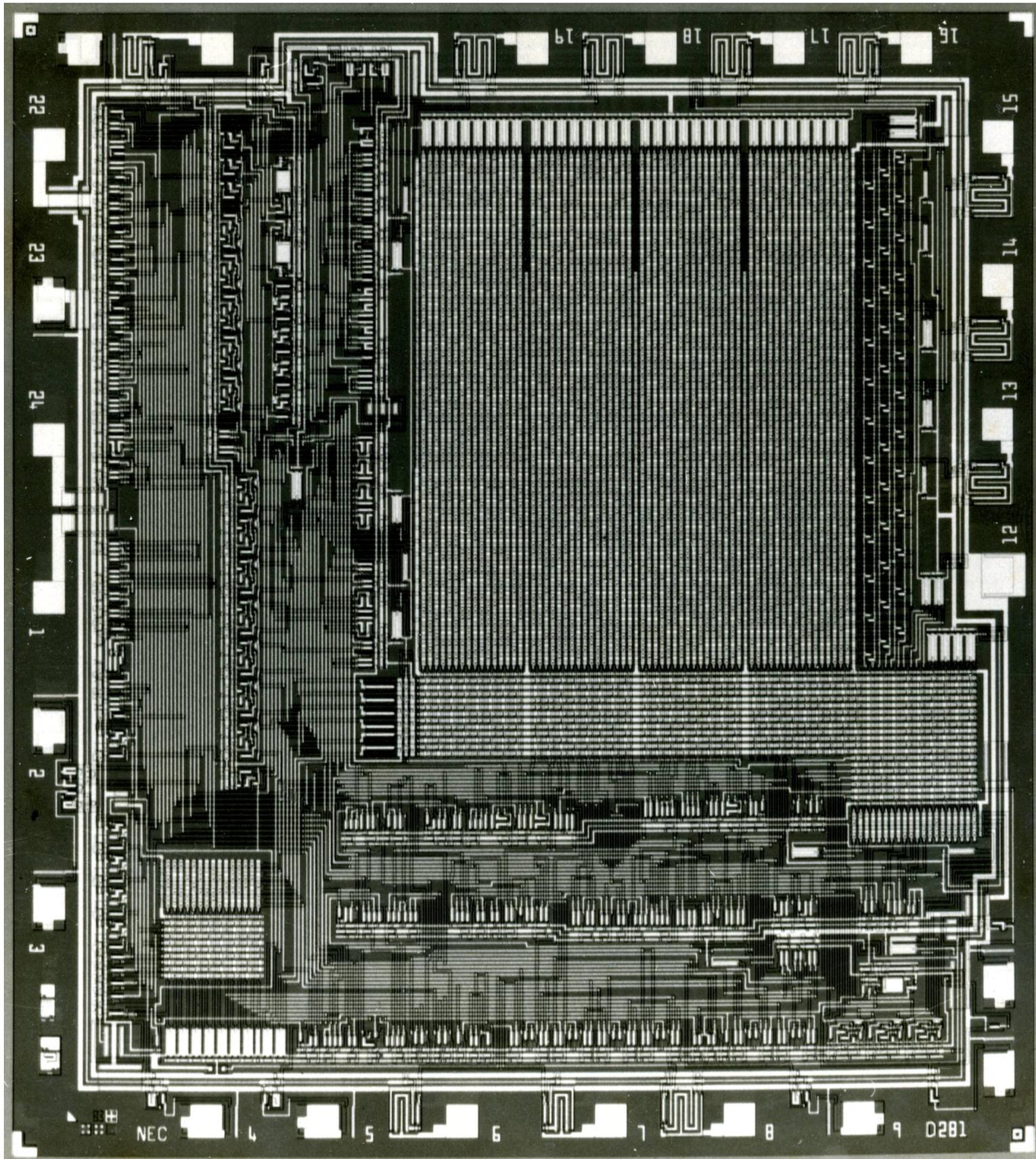
On PDF Viewer,

(1) Maximize window

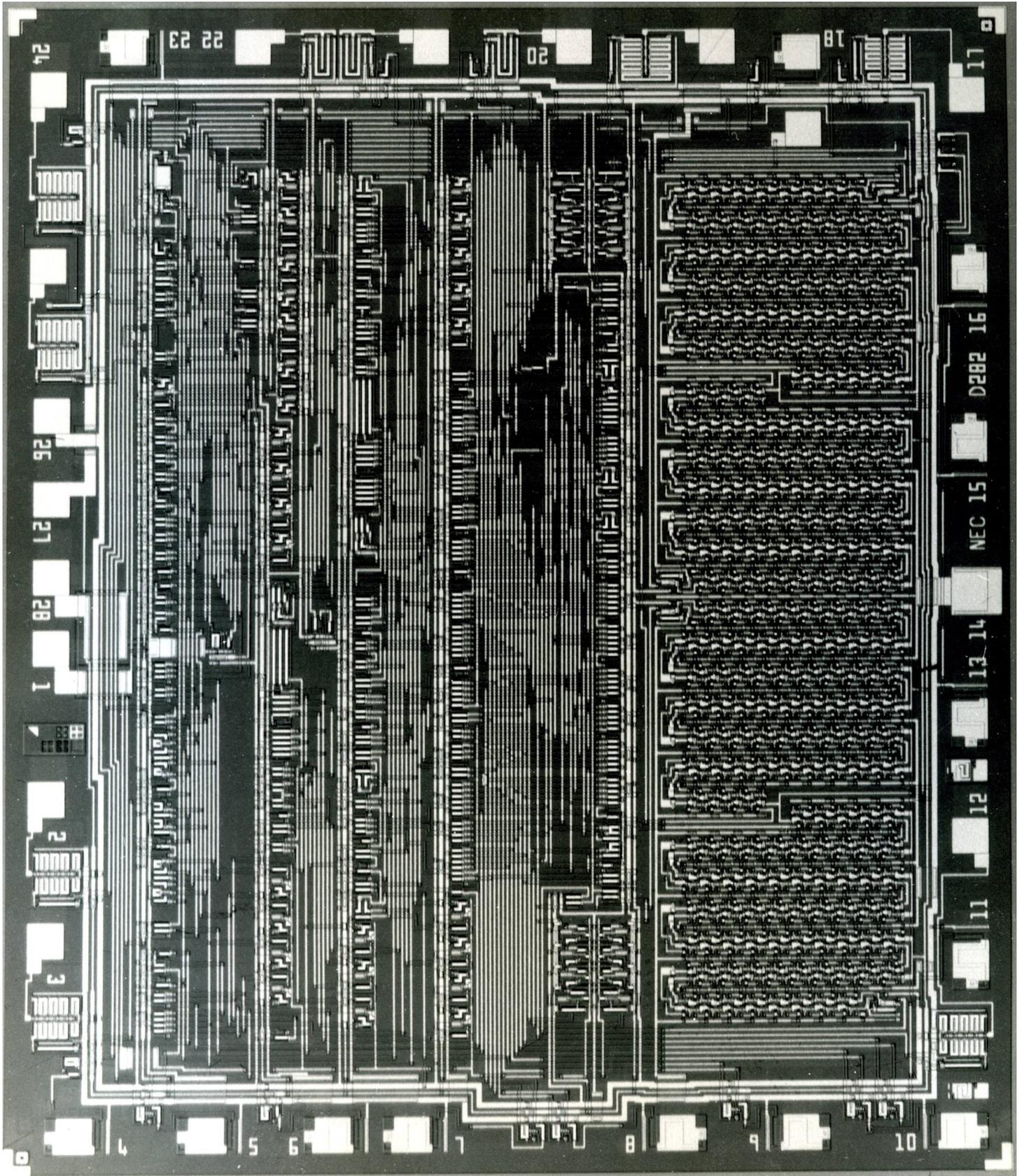
(2) View – Page Display – Two Page Scrolling

To watch a micrograph of  $\mu$ PD282D manufactured and based upon this logic and mask layout design,

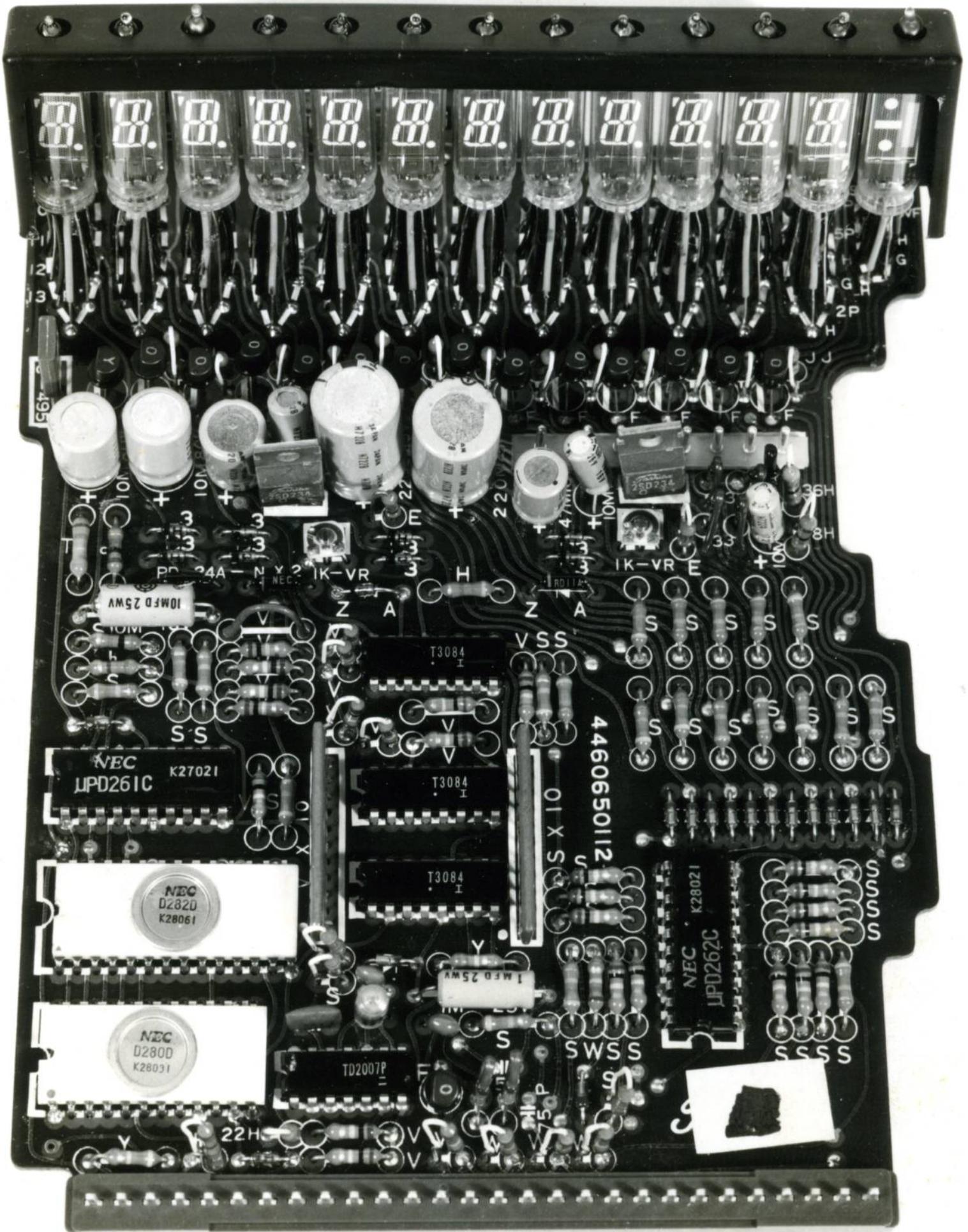
go to [https://www.oguchi-rd.com/Accomplishment/Projects/Die Photo by Hugin/ \$\mu\$ PD282D\\_20x Micrograph.pdf](https://www.oguchi-rd.com/Accomplishment/Projects/Die Photo by Hugin/<math>\mu</math>PD282D_20x Micrograph.pdf).



μPD281



μPD282



Mother board of 12 digit 1 memory desk-top calculator assembled  $\mu$ PD282



Whole aspect of 12 digit 1 memory desk-top calculator