# United States Patent [19]

## Oguchi

[56]

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[75]	Inventor:	Tetsuji Oguchi, Tokyo, Japan		
[73]	Assignee:	Nippon Electric Co., Inc., Tokyo, Japan		
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		arch 364/200, 900		

[54] INFORMATION PROCESSING APPARATUS

#### **References Cited**

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Primary Examiner—Gareth D. Shaw Assistant Examiner—John G. Mills Attorney, Agent, or Firm—Sughrue, Mion, Zinn, Macpeak, and Seas

### [57] ABSTRACT

An information processing apparatus employs first and second direct memory access controllers which cooperate during transfer of information between first and second devices, e.g. memories. The first controller controls information transfer from the first memory to the second controller and the second controller transfers the information from its own internal storage to the second memory while simultaneously receiving further information under the control of the first controller. The second controller includes address control circuitry for high speed generation of non-sequential addresses for writing into the second memory.

### 1 Claim, 8 Drawing Figures

