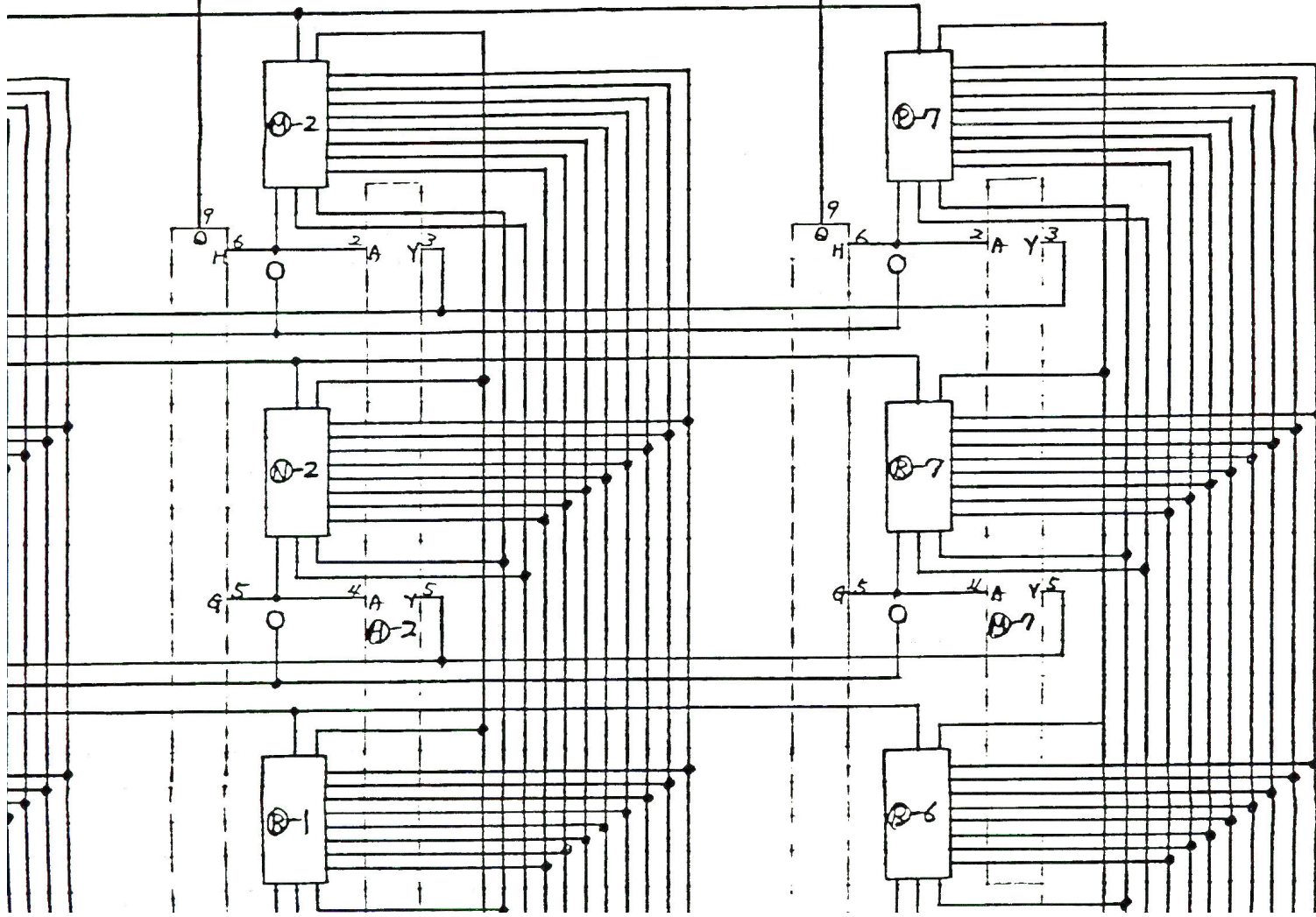


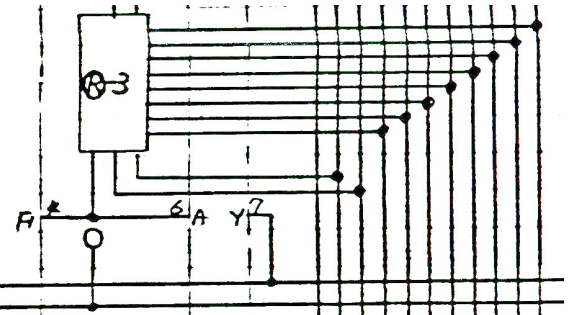
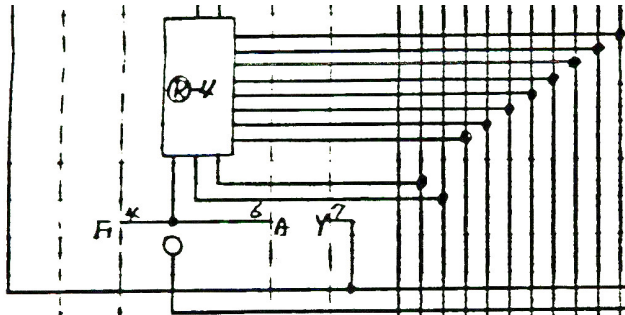
B



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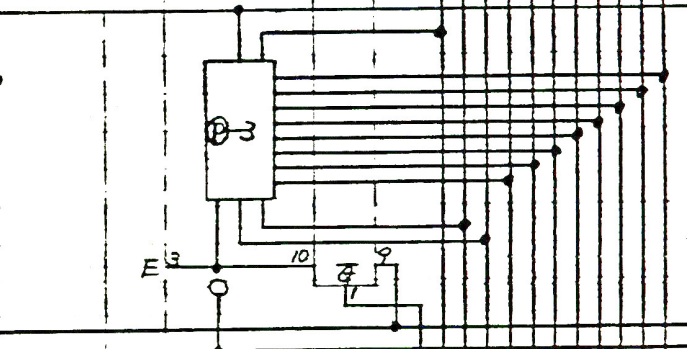
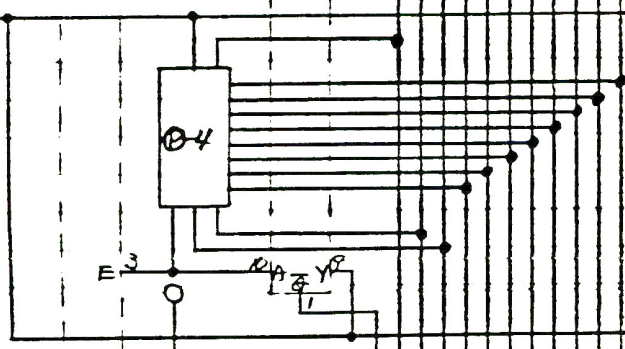


D2*



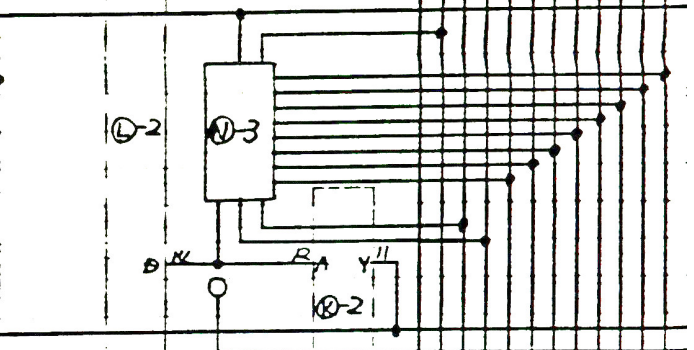
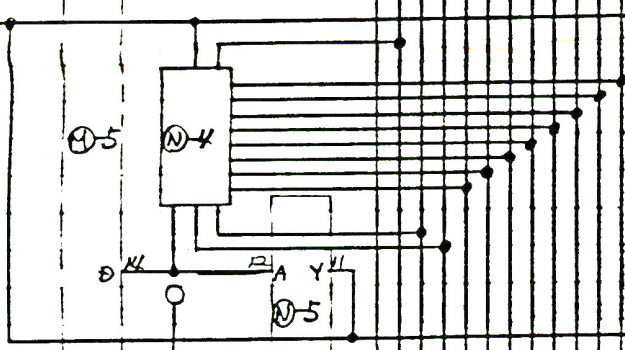
D3*

D3*



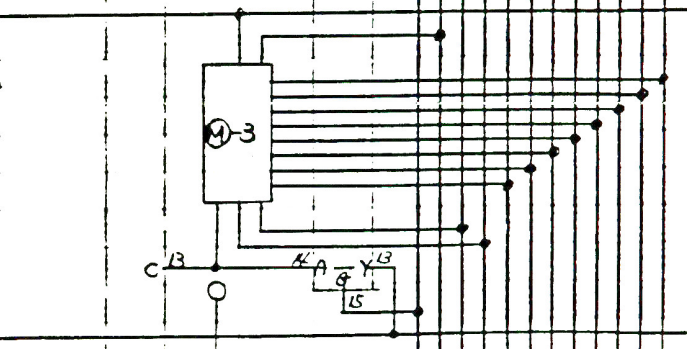
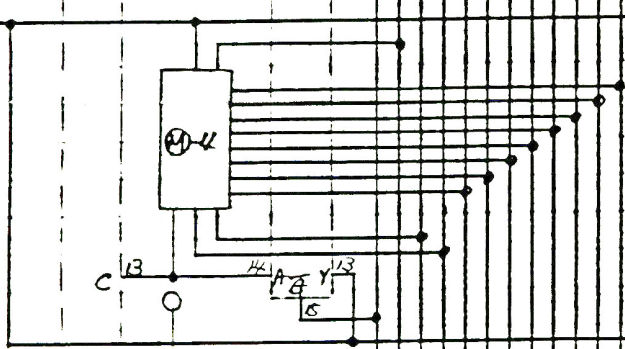
D4*

D4*



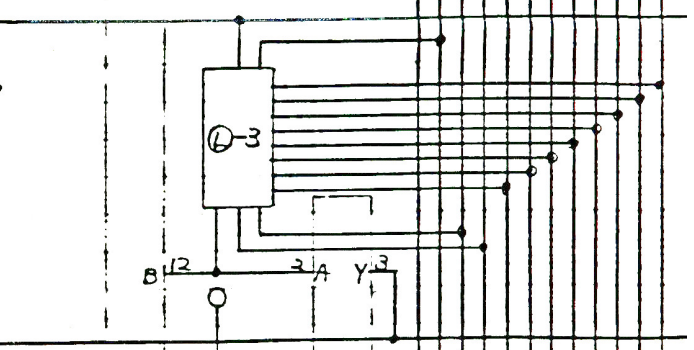
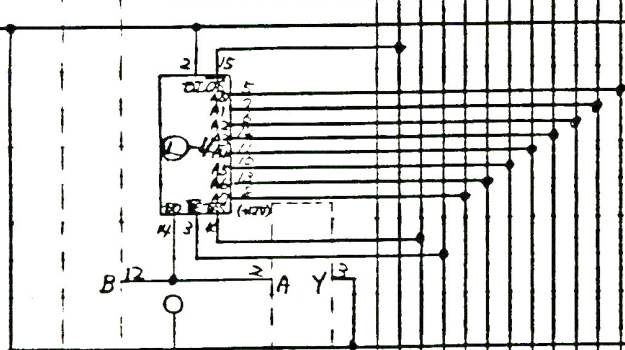
D5*

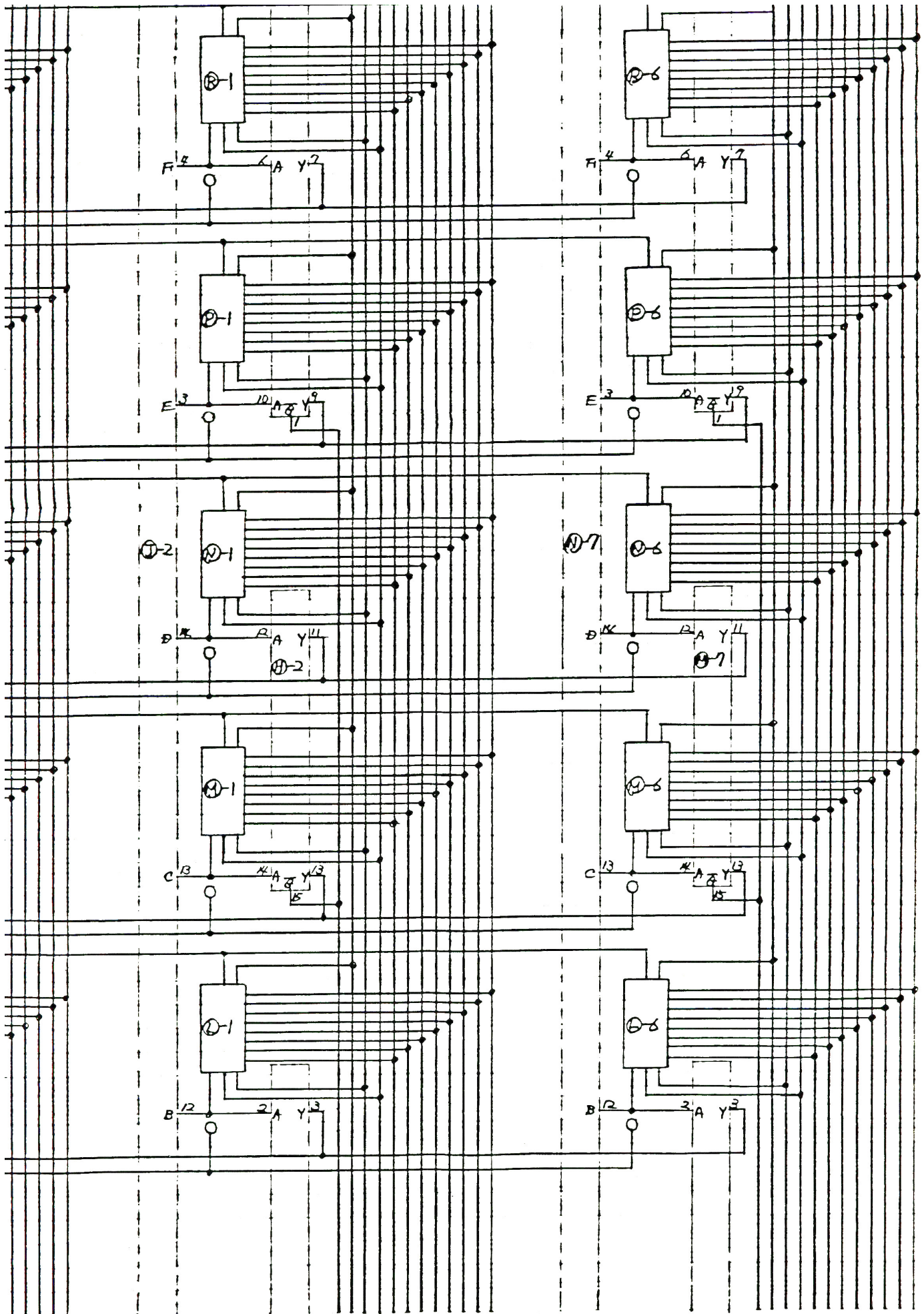
D5*



D6*

D6*

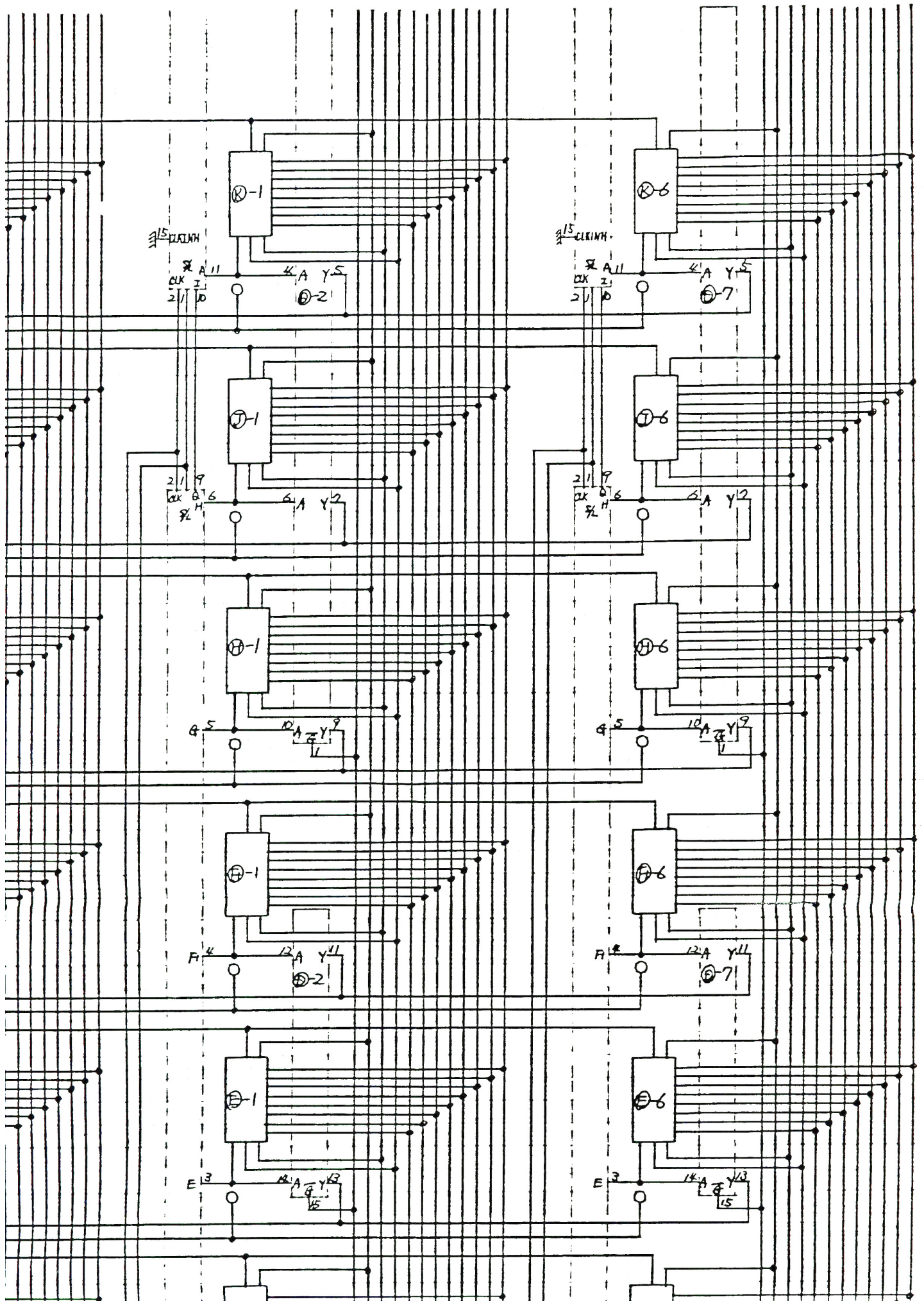


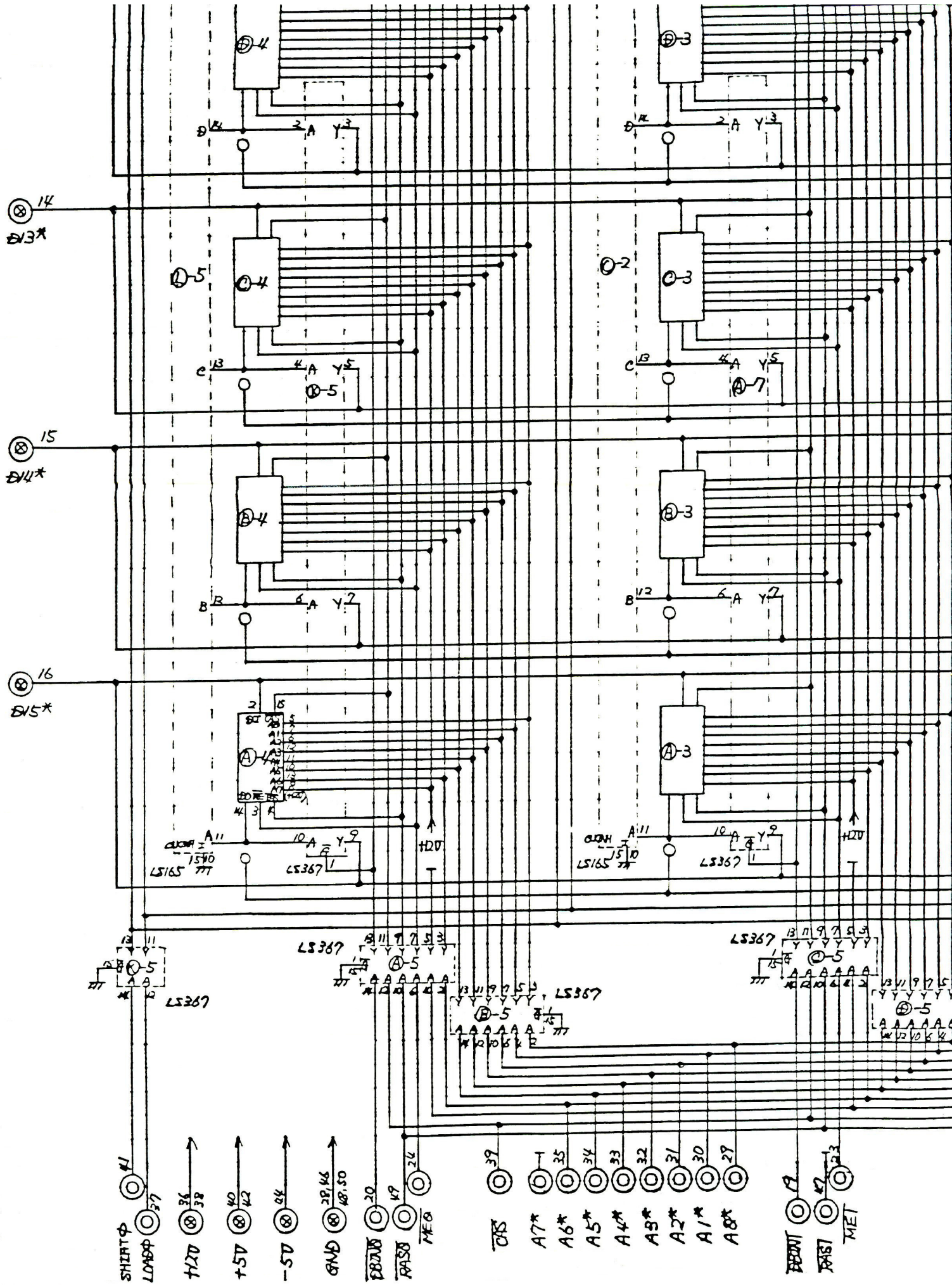


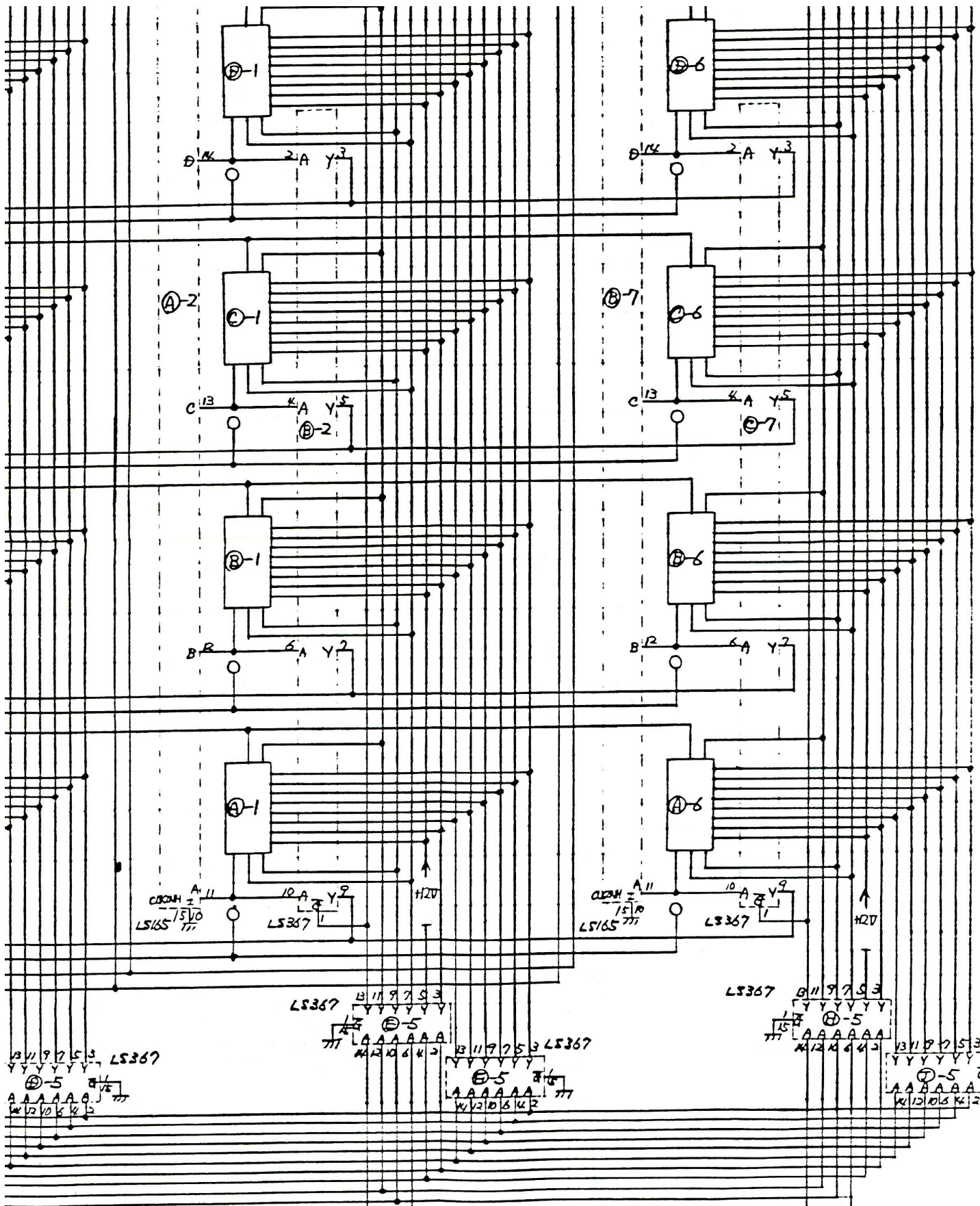
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54.11.6.
55.3.5.37



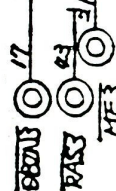
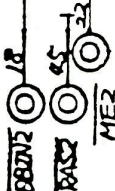




LS367
 13 11 9 7 5 3
 Y Y Y Y Y Y
 @-5
 A A A A A A
 W 12 10 6 4 2

LS367
 13 11 9 7 5 3
 Y Y Y Y Y Y
 @-5
 A A A A A A
 W 12 10 6 4 2

LS367
 13 11 9 7 5 3
 Y Y Y Y Y Y
 @-5
 A A A A A A
 W 12 10 6 4 2



D RAM 1/2

1.24.1981.

54.11.6
 55.3.5.22

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