Sanjay Mehrotora

Job Experience

Micron Technology, CA
- President & CEO   May 2017 – Present

Cavium Inc., CA
- Board Member   2009 – Present

Western Digital, CA
- Board Member   May 2016 – Feb 2017

SanDisk, CA
- Co-founder, President & CEO   Nov 1888 – May 2016

Atmel Corporation, CA
- Consultant   April 1988 – Nov, 1888

Integrated Device Technology, CA
- Design Manager   Sep 1986 – April 1988

Seeq Technology, CA
- Design Manager & Application Engineering Manager   Feb 1982 – Sep 1986

Intel Corporation, CA
- Senior Design Engineer   Jan 1980 – Dec 1981

Education

University of California, Berkeley, CA
- Master of Science, Electrical Engineering and Computer Science   1978 – 1980

University of California, Berkeley, CA
- Bachelor of Science, Electrical Engineering and Computer Science   1976 – 1978

Birla Institute of Technology and Science, Pilani, India   1974 – 1976

Khandker Quader

Job Experience

Independent Consultant

July 2014 – Present

SK Hynix Memory Solutions (US subsidiary of SK Hynix), CA
• President & CEO

June 2013 – June 2014

SanDisk, CA
• Sr. Vice President of Memory Organization

Aug 2007 – Apr 2013
• Vice President of NAND Flash Memory Design, Product Development, & R&D

Jan 2005 – Aug 2007
• Sr. Director, NAND Flash Memory Design & Technology Development

Sept 2001 – Dec 2004
• Director, NAND Flash Memory Design

Feb 2000 – Aug 2001
• Design manager

Jun 1997 – Jan 2000
• Project Manager

Jun 1995 – May 1997

IBM, SRDC (Semiconductor Research and Development Center; DRAM R&D), NY
• Senior Engineer

1993 – 1995

Intel Corporation, Memory Division (SRAM & NVM), CA
• Design Manager

1984 – 1989

Education

University of California, Berkeley, CA
• Doctor of Philosophy, Electrical and Computer Engineering

1989 – 1993

Georgia Institute of Technology, GA
• Master of Science, Electrical Engineering

1982 – 1984

Massachusetts Institute of Technology, MA
• Bachelor of Science, Electrical Engineering and Physics

1979 – 1982