



シミュレーション・テストパターン

UPD7220 (ADC) 開発履歴概要



	1978	1979	1980	1981	1982
Jan				- Trial production #3 (4 $\mu$ )	- <a href="#">Seminar at JIEC</a>
Feb		- Meeting with Intel #2 at NEC	- Start of breadboard debug	- ISSCC - Start of sales promotion for external companies	
Mar					
Apr		- Meetings with peripheral division	- Completion of breadboard debug	- Trial production #4 (4 $\mu$ )	
May		- Meetings with in-house divisions		- Electronics magazine - Start of sampling	
Jun			- Mask layout review session		
Jul		- Meeting with Intel #3 at NEC - Issuance of system specification	- Completion of mask layout	- Trial production #1 (3 $\mu$ )	
Aug			- Issuance of evaluation specification		
Sep			- Trial production #1 (4 $\mu$ )	- WESCON	
Oct		- Completion of logic design - Logic design review session	- GDC Level 2 tester to Intel (Intel MDS 80 convertible)	- <a href="#">Nikkei Electronics</a>	
Nov			- Trial production #2 (4 $\mu$ )		
Dec	- Meeting with Intel #1 at Intel			- Trial production #2 (3 $\mu$ ) - Mass production transfer (3 $\mu$ )	

**Abstract History of  $\mu$ PD7220 (Graphics Display Controller) Development** (from 12/1978 – 1/1982)